

FIGURE 1

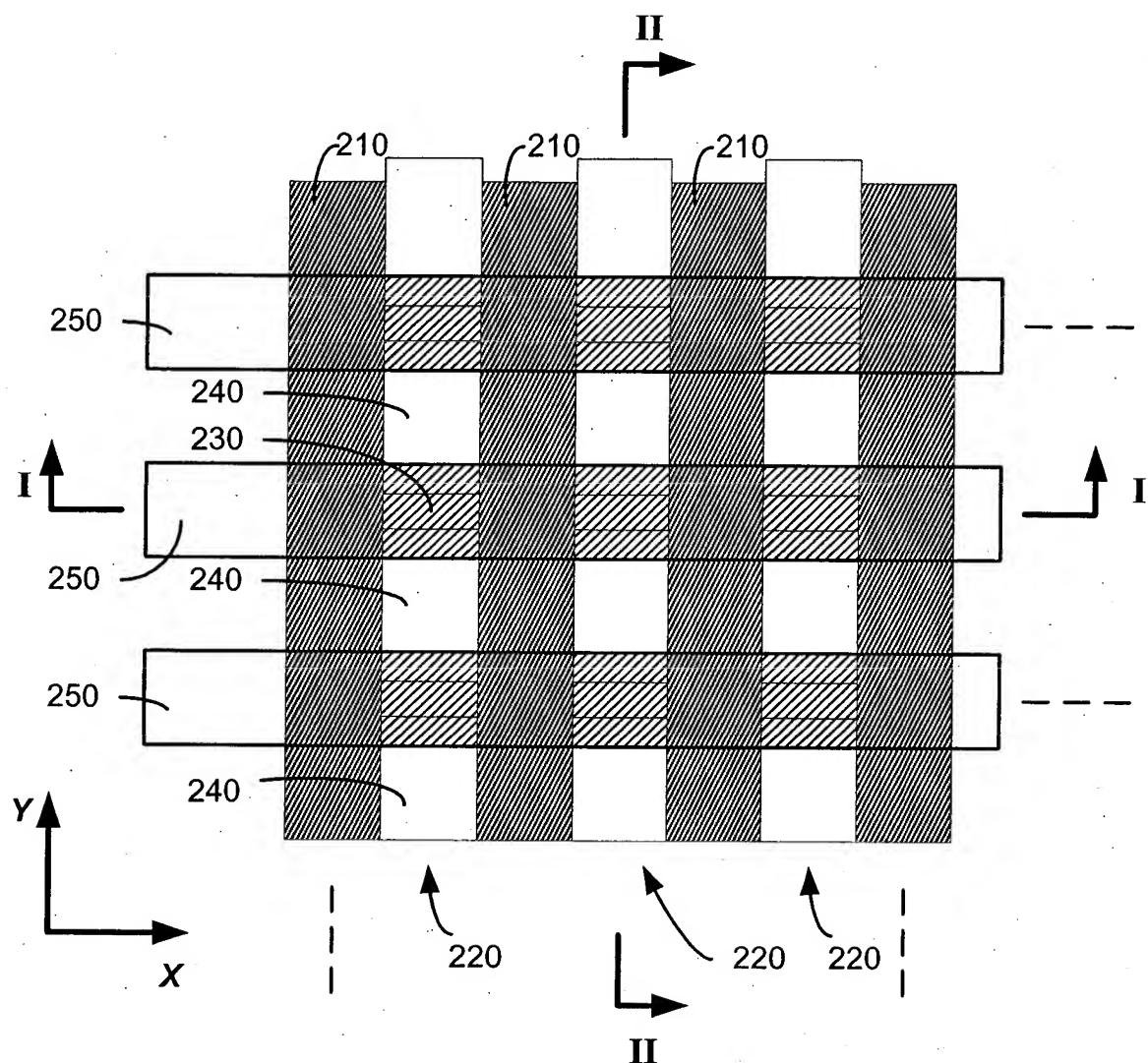


FIGURE 2(A)

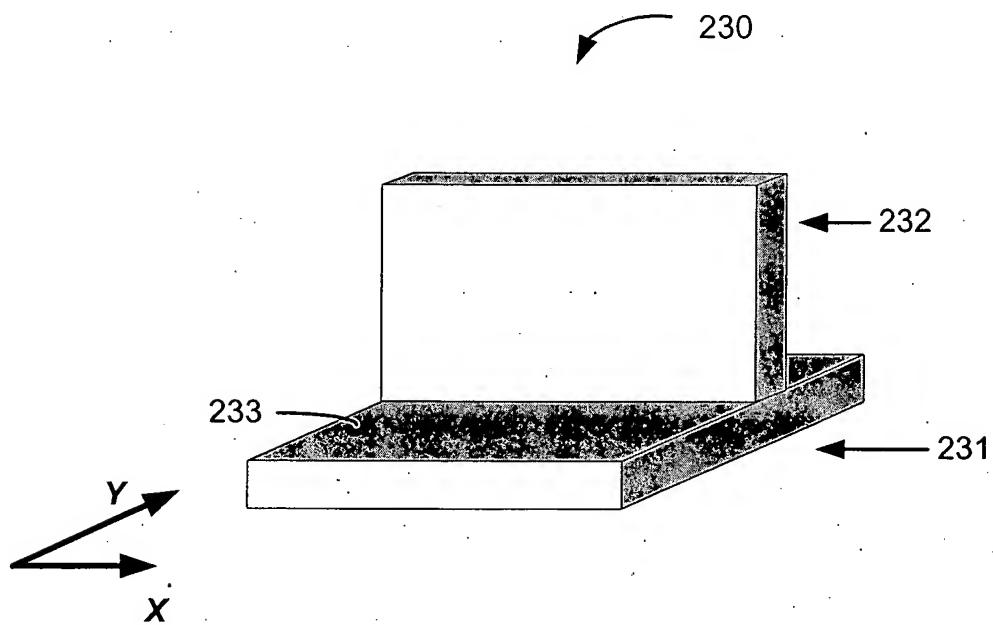


FIGURE 2(B)

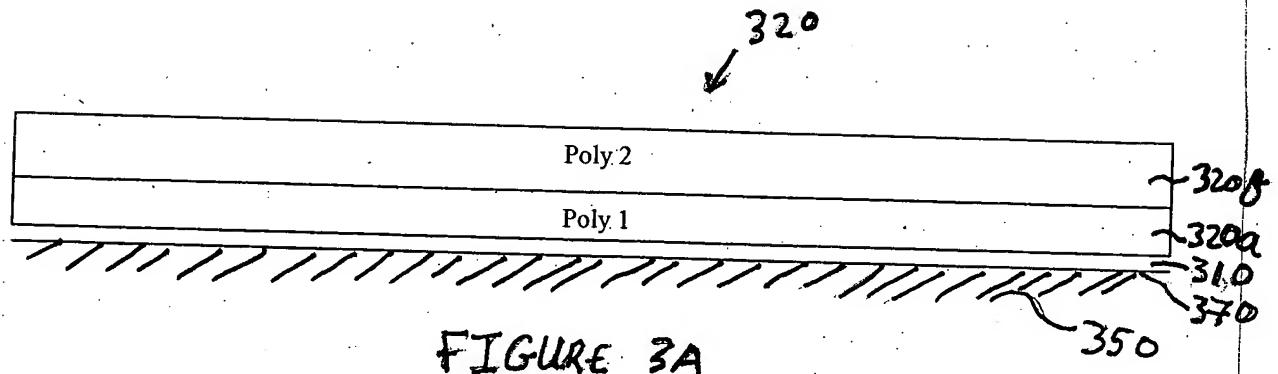


FIGURE 3A

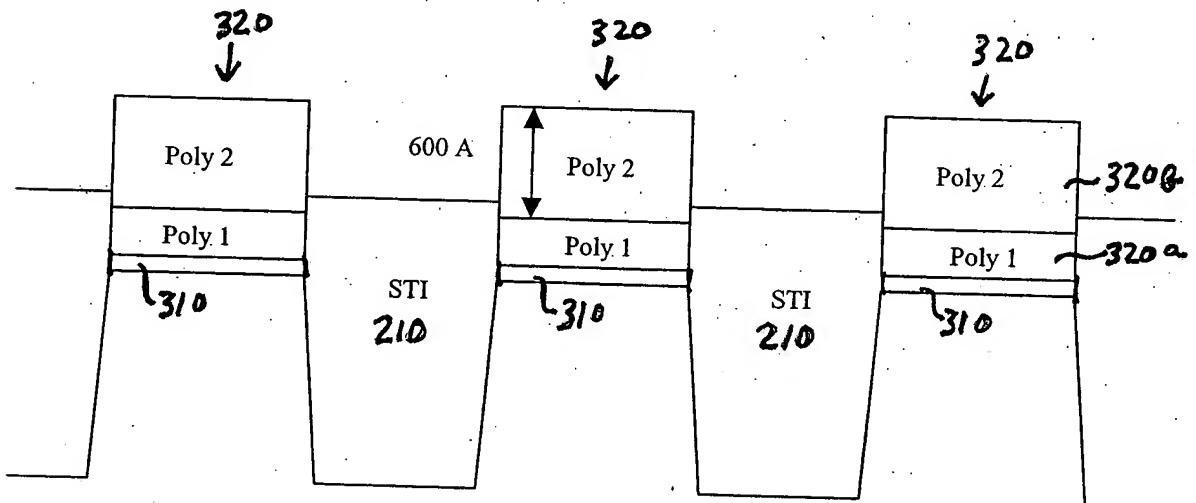


FIGURE 3B

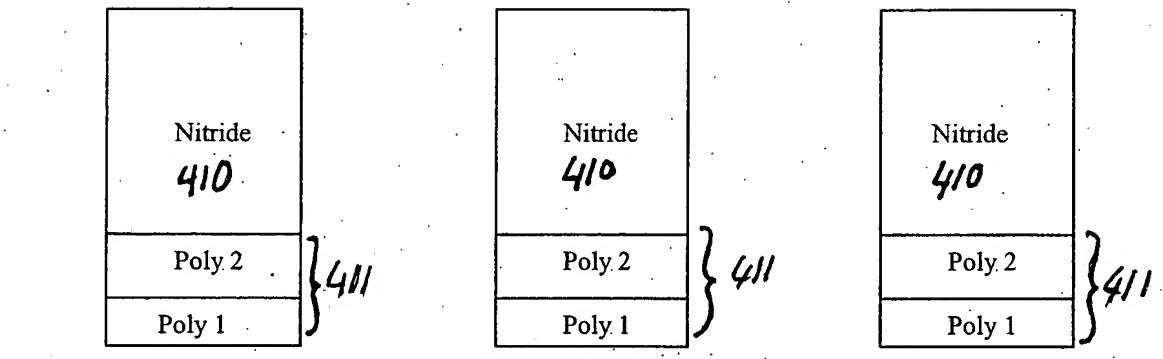


FIGURE 4A

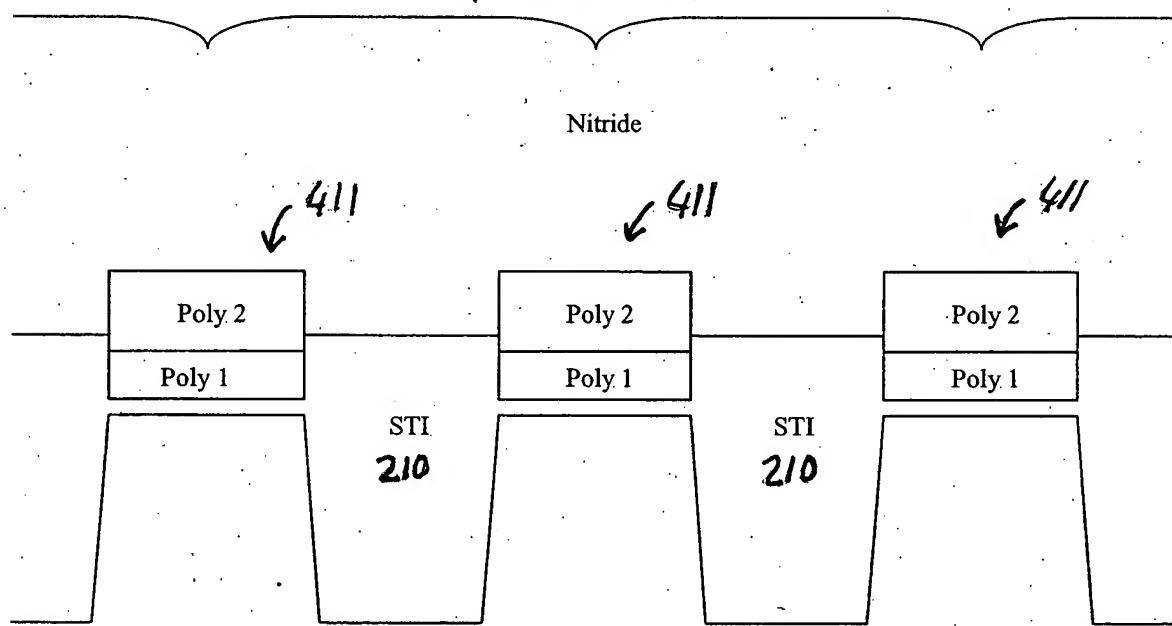


FIGURE 4B

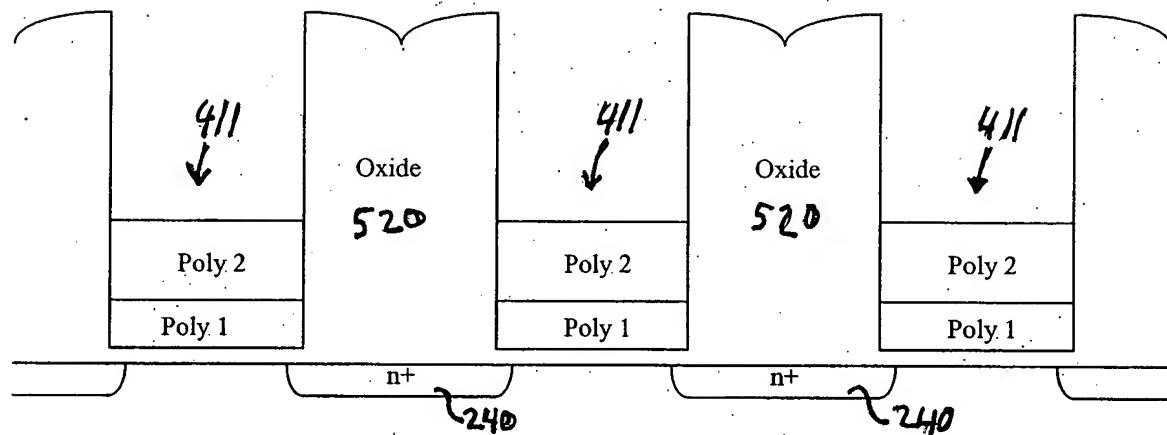


FIGURE 5A

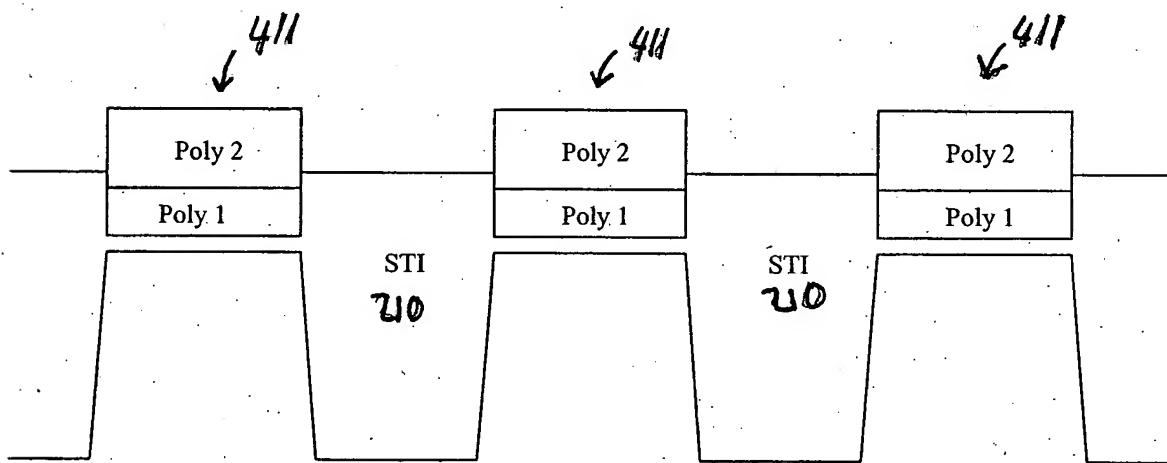


FIGURE 5B

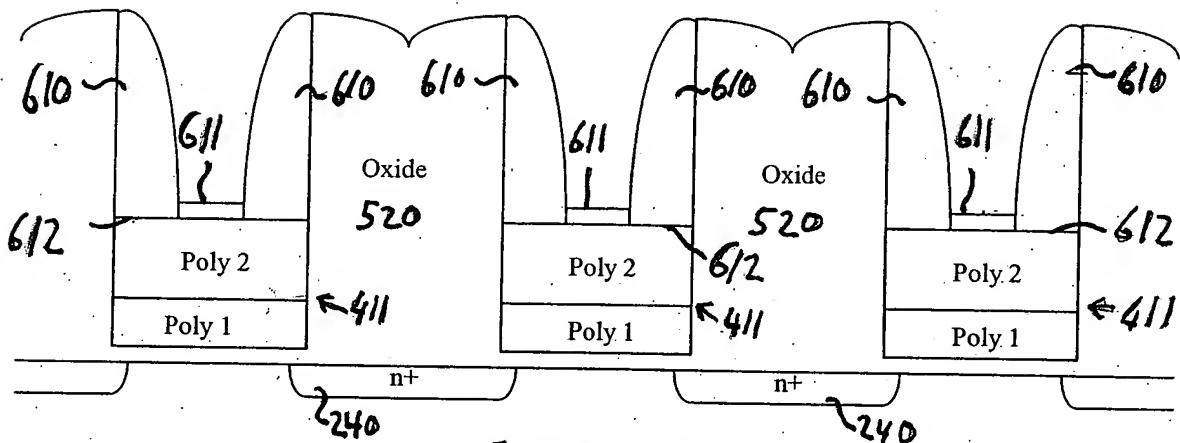


FIGURE 6A

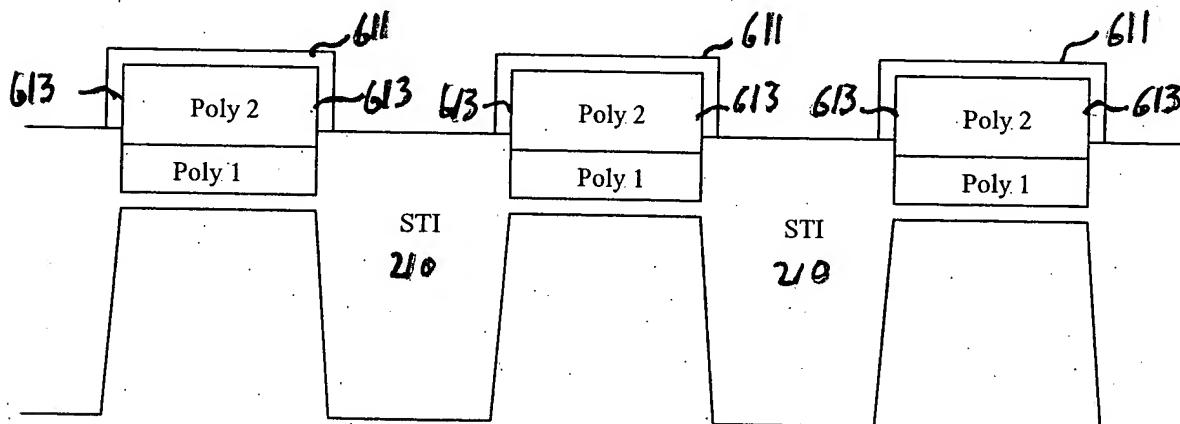


FIGURE 6B

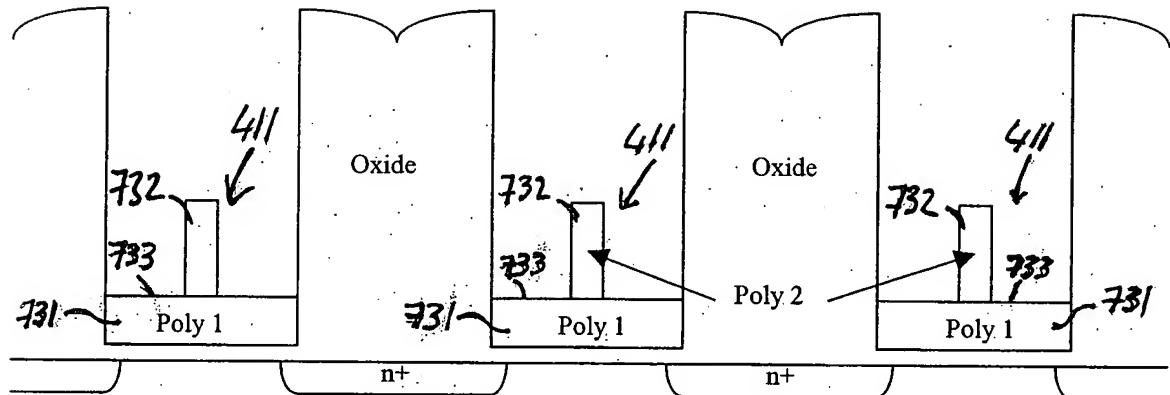


FIGURE 7A

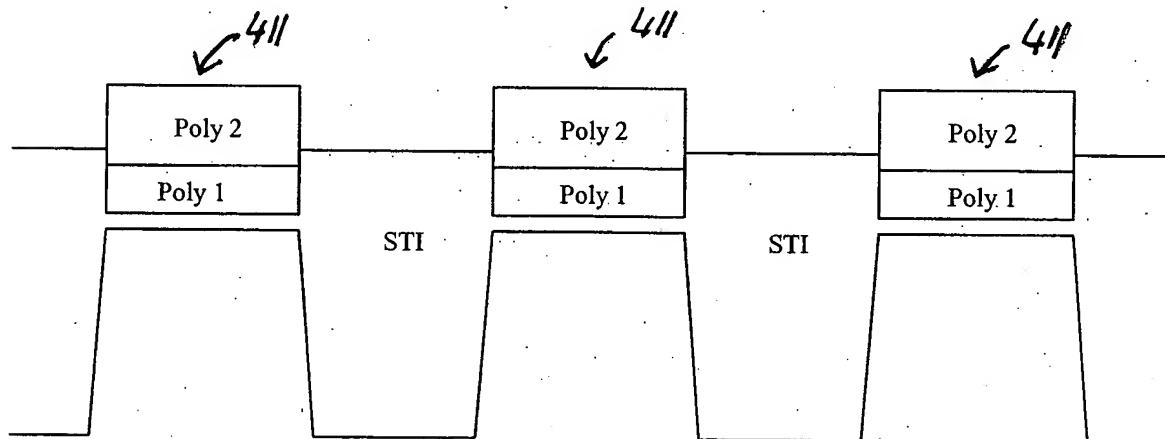


FIGURE 7B

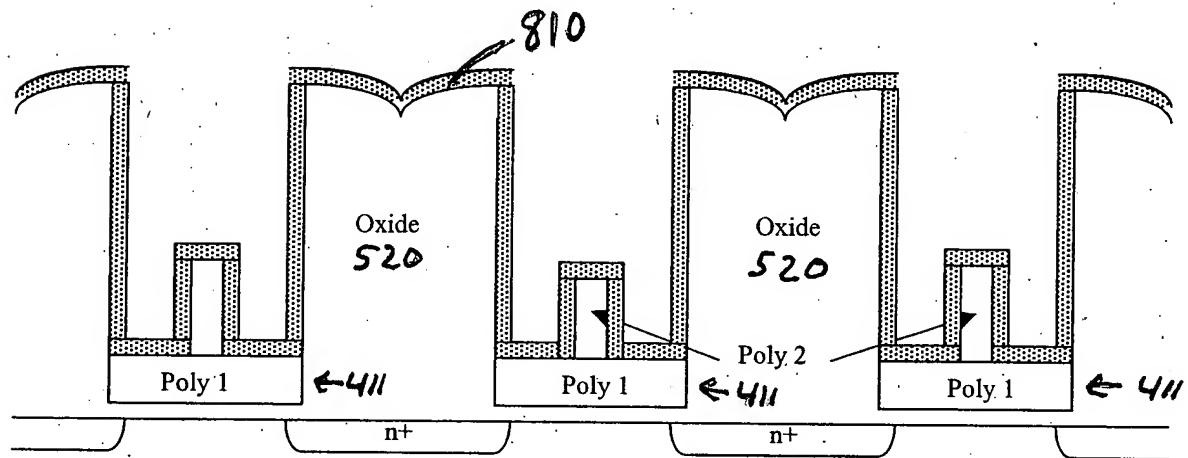


FIGURE 8(A)

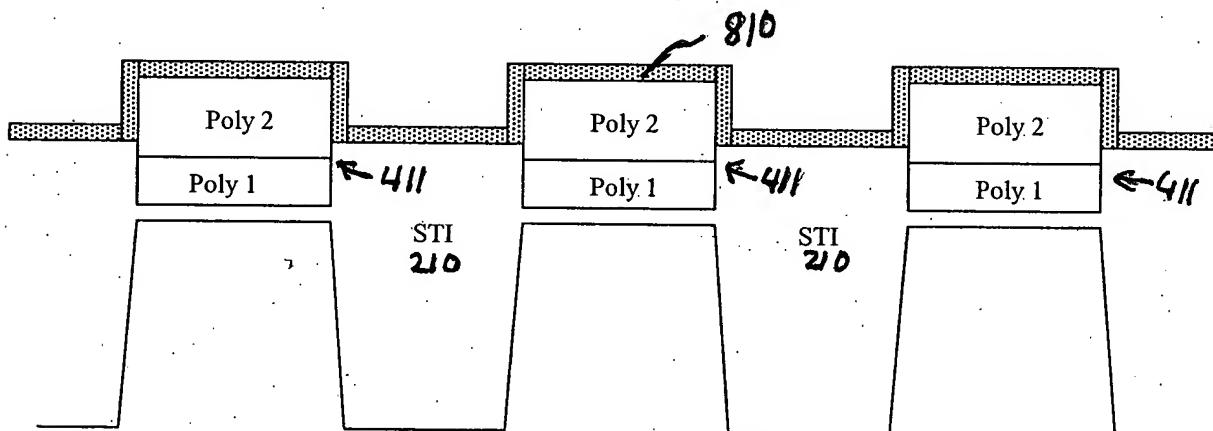


FIGURE 8(B)

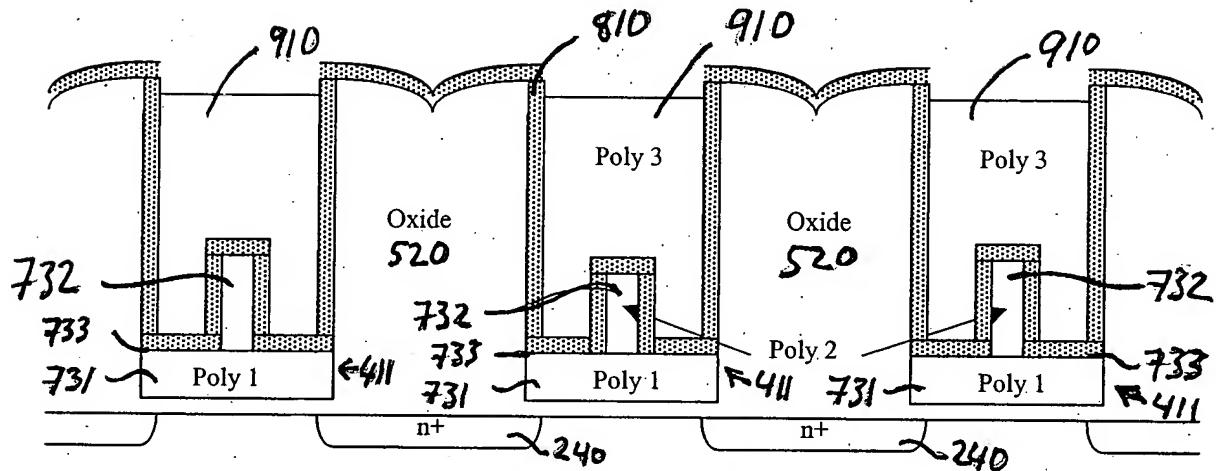


FIGURE 9(A)

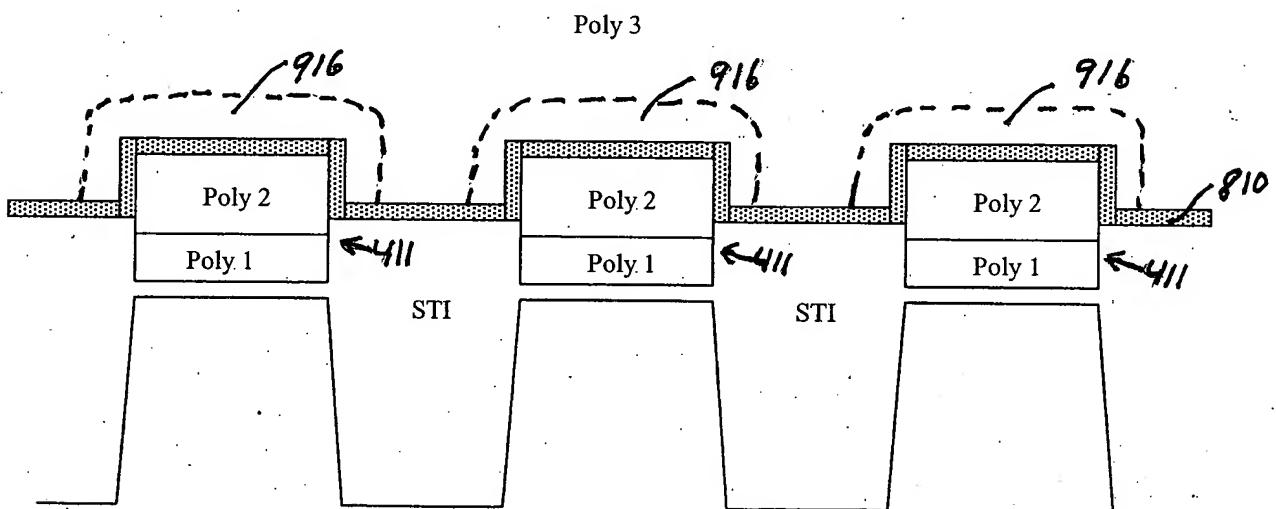


FIGURE 9(B)

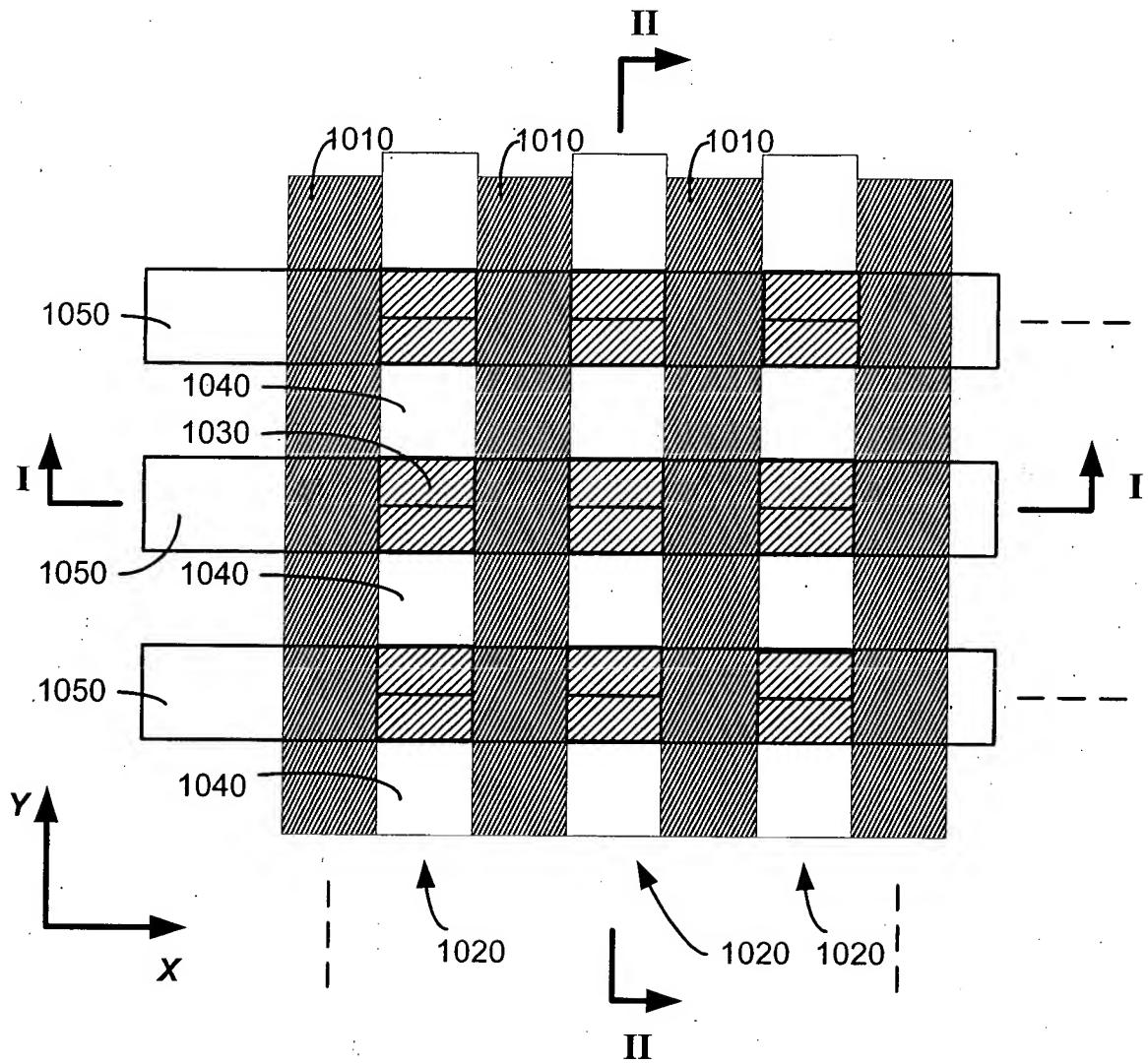


FIGURE 10(A)

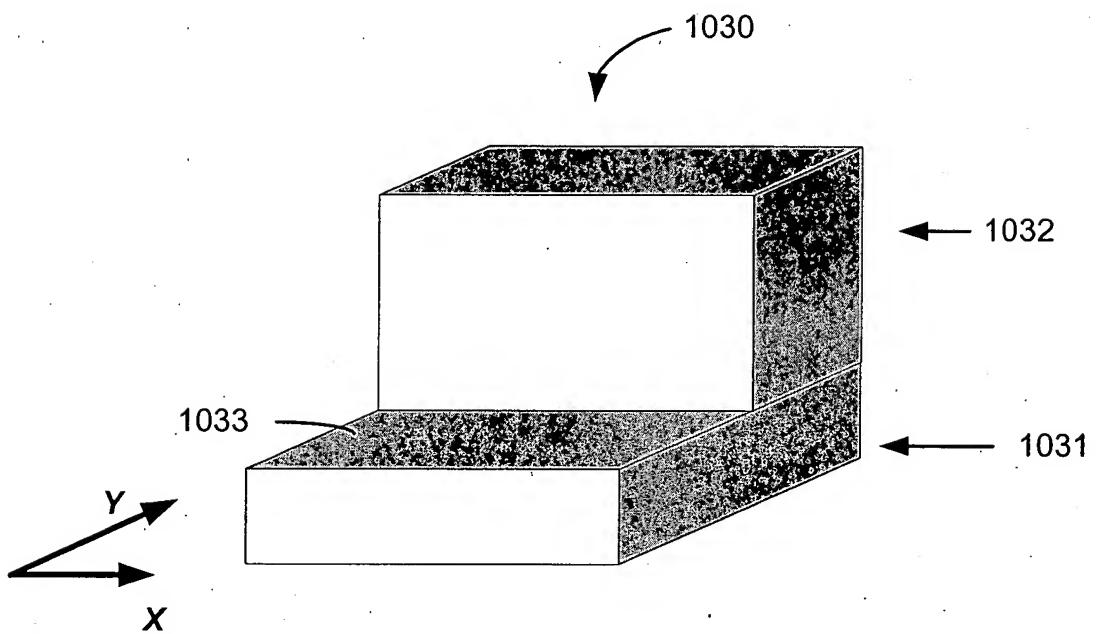


FIGURE 10(B)

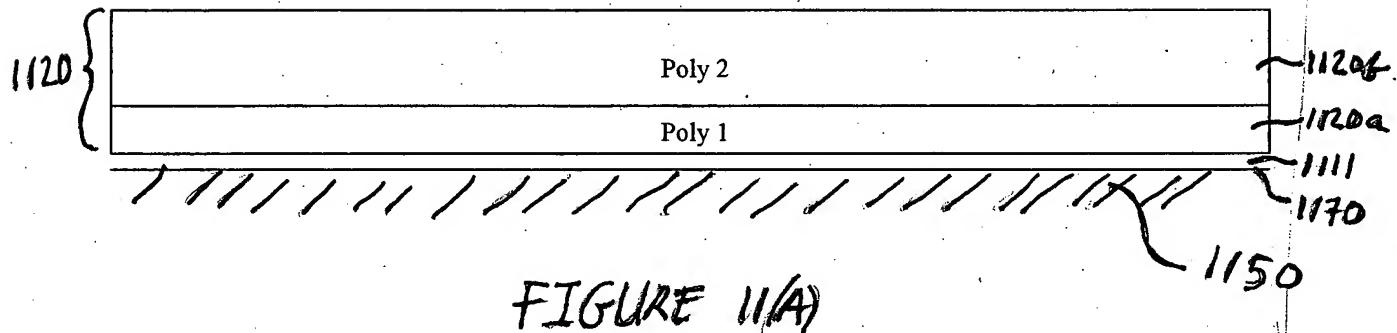


FIGURE 11(A)

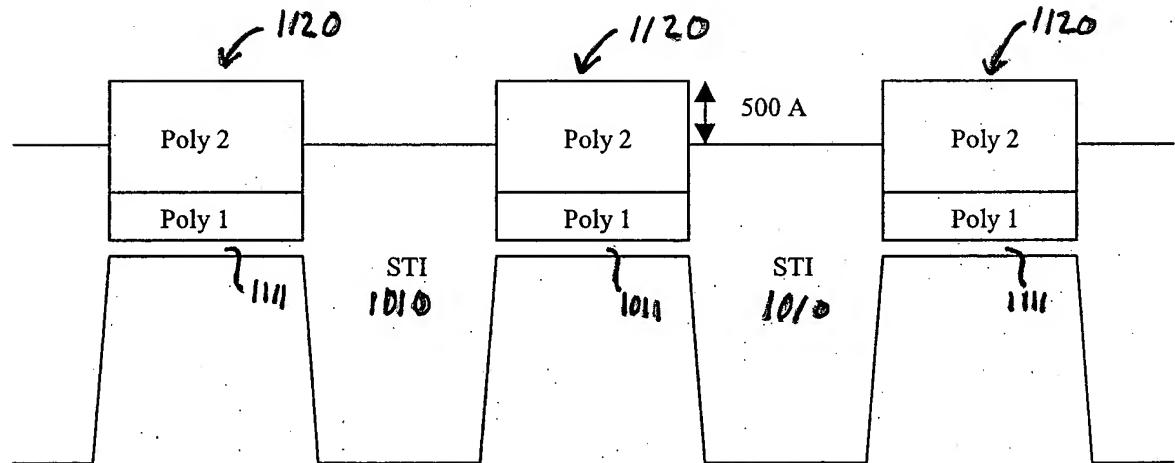


FIGURE 11(B)

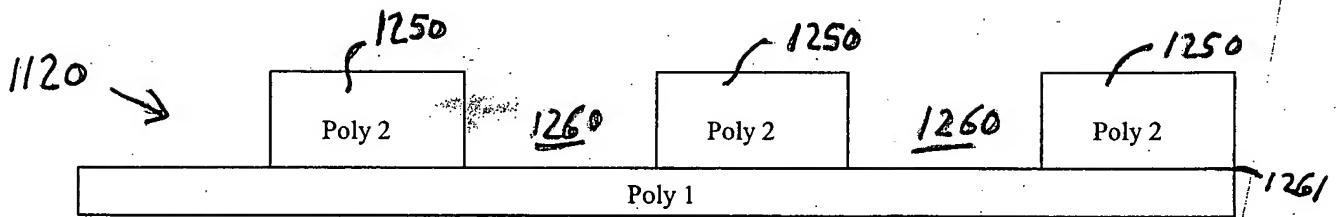


FIGURE 12(A)

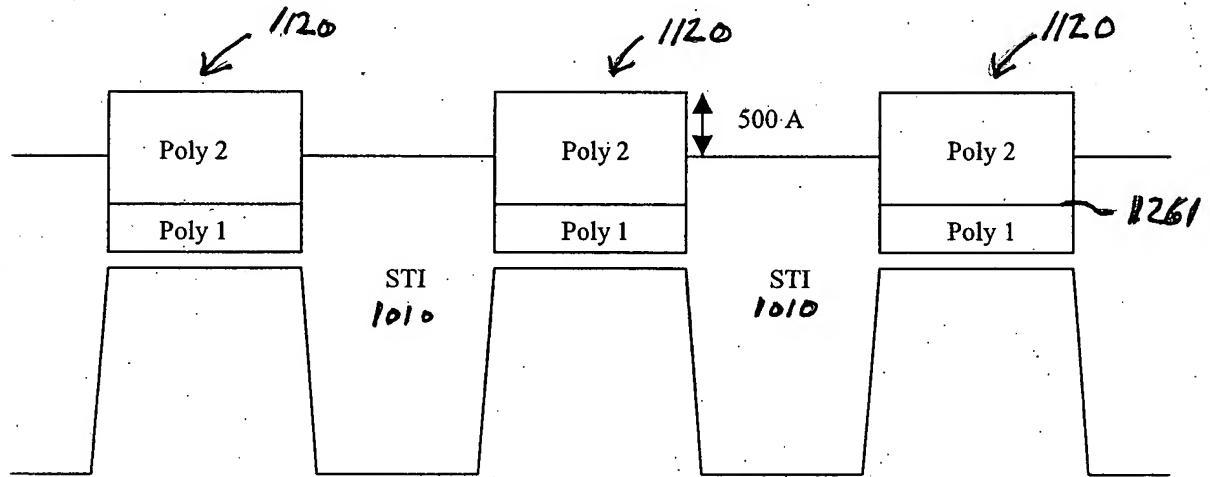


FIGURE 12(B)

WSi 1340

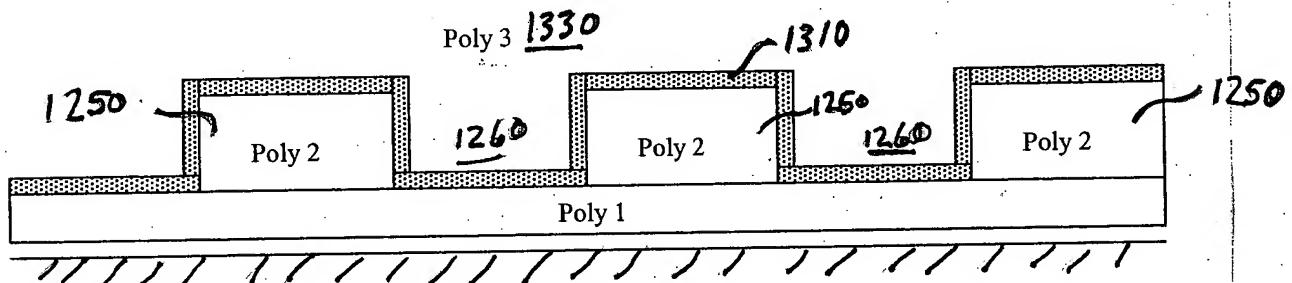


FIGURE 13(A)

WSi 1340

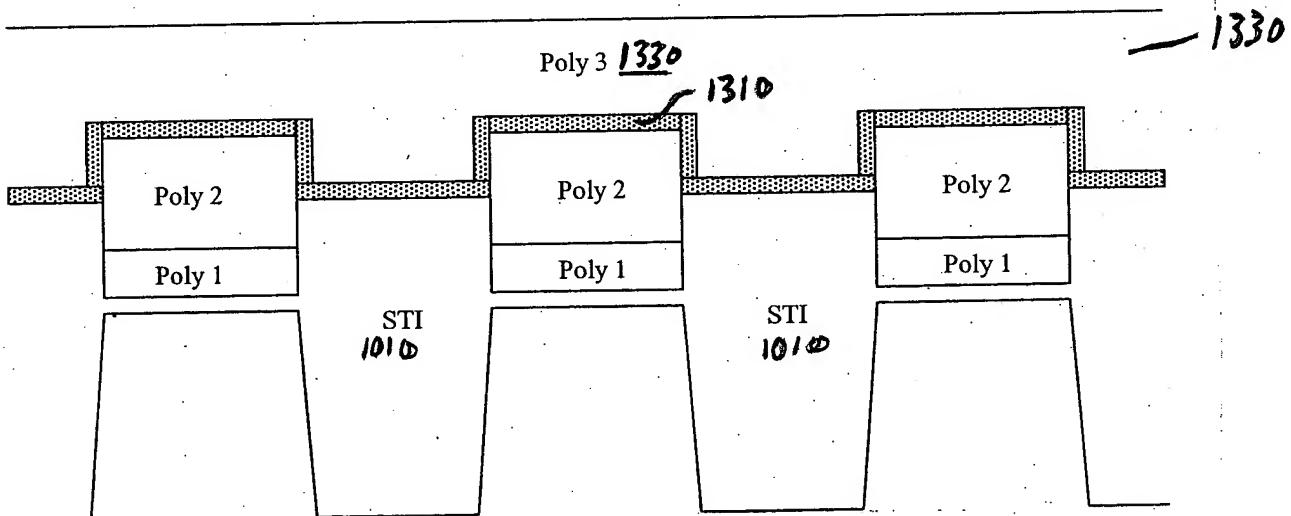


FIGURE 13(B)

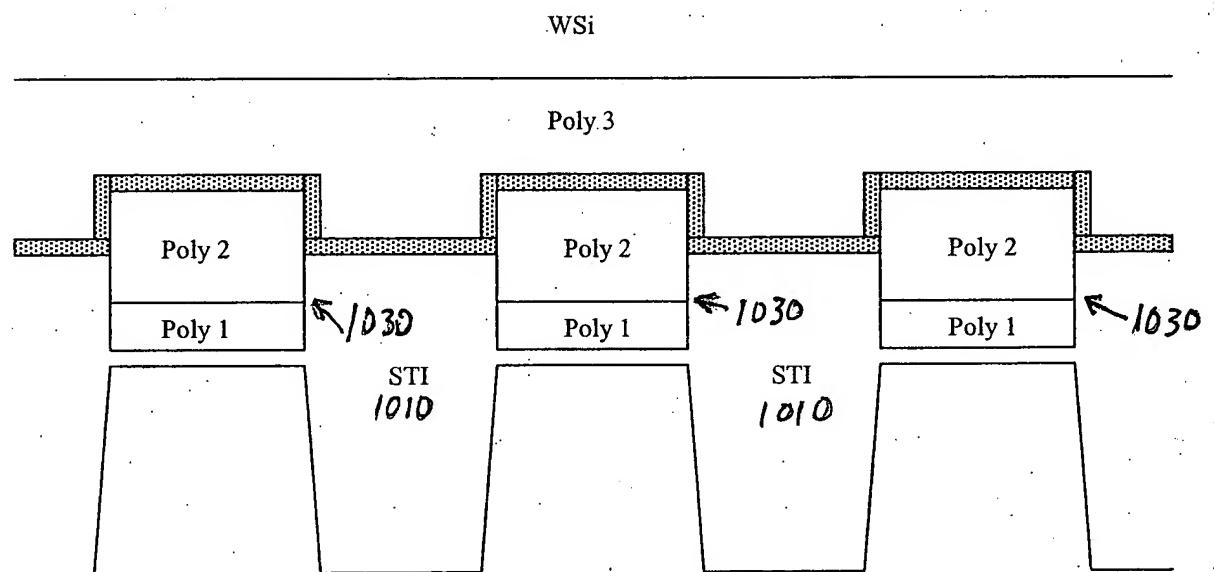
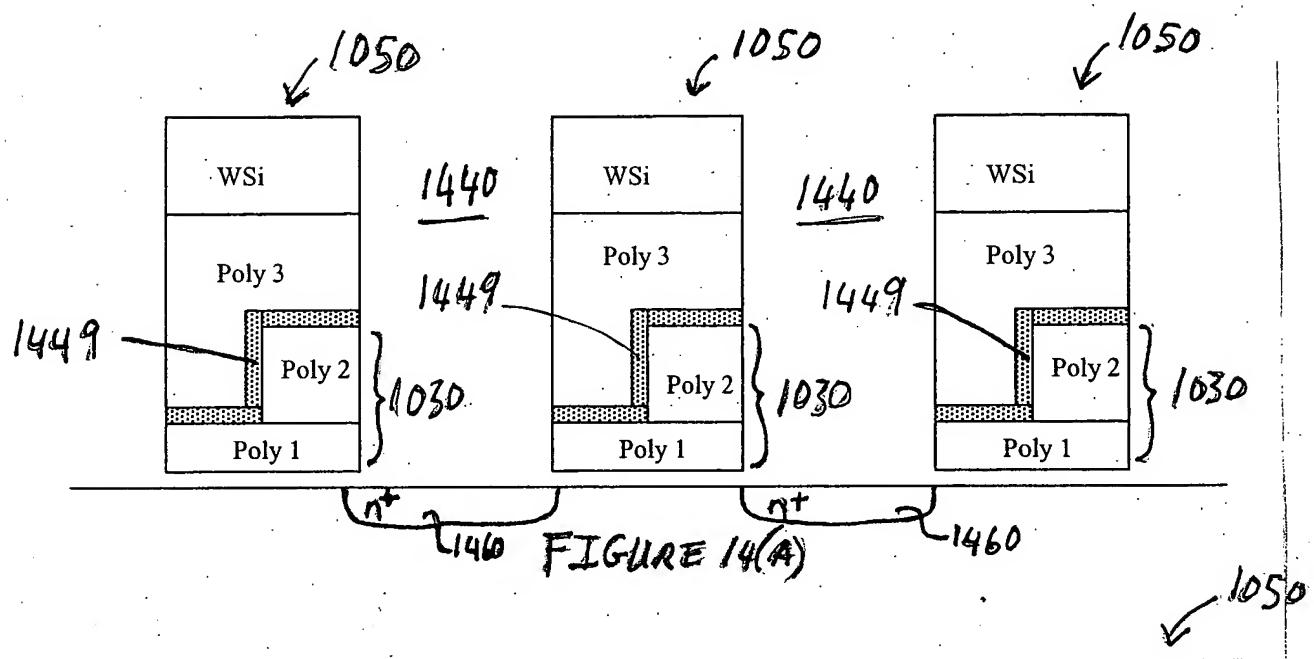


FIGURE 14(B)